

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L281	21	hardware with (implement\$6) near2 (orb object adj request adj broker)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:29
L282	68	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:29
L283	1	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and pluggable	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:30
L284	17	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and corba	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:30
L285	11	284 not 281	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:30
L286	8	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and sca	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:31

L287	13	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and waveform	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:31
L288	13	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and waveform	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:31
L289	14	(asic fpga hardware chip circuit) near3 (orb object adj request adj broker) and (sca jtrs waveform)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:32
L290	19	(asic fpga hardware chip circuit dsp gate) near3 (orb object adj request adj broker) and (sca jtrs waveform)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 20:32

1/16/2009 8:33:49 PM

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